

	Type	L #	Hits	Search Text	DBs	Time Stamp
4	BRS	L41	1815	((emitter adj coupled adj logic) ecl) and (bipolar) and (fet mosfet pmos nmos mos)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/28 11:10
5	BRS	L42	1117	41 and resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/28 11:10
6	BRS	L43	785	42 and load	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/28 11:10
7	BRS	L44	537	43 and (current adj source)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/28 11:11
8	BRS	L45	312	44 and (collector and base and emitter and drain and gate and source)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/28 11:12